

WEST Search History

DATE: Tuesday, May 31, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L53	5805878.UREF.	21
<input type="checkbox"/>	L52	5805878.pn.	1
<input type="checkbox"/>	L51	5740415.uref.	6
	<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L50	branch\$3 same L49	2
<input type="checkbox"/>	L49	"same block address"	89
<input type="checkbox"/>	L48	(L45 or L46) same branch\$3	5
<input type="checkbox"/>	L47	"same bundle address"	0
<input type="checkbox"/>	L46	"same cache line address"	29
<input type="checkbox"/>	L45	"same line address"	70
<input type="checkbox"/>	L44	5805878.uref.	21
<input type="checkbox"/>	L43	L40 same Simultaneous\$2	34
<input type="checkbox"/>	L42	L40 same L41	0
<input type="checkbox"/>	L41	"same address"	14322
<input type="checkbox"/>	L40	(multiple branch\$2) with predict\$3	133
	<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L39	branch\$3 and L38	17
<input type="checkbox"/>	L38	fetch\$3 with correct\$3 with address\$3	98
<input type="checkbox"/>	L37	fetch\$3 with correct\$3 with L21	0
<input type="checkbox"/>	L36	correct\$3 with bundle with address\$3	4
<input type="checkbox"/>	L35	branch\$3 and L34	9
<input type="checkbox"/>	L34	correct\$3 with line with address\$3	480
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L33	L32 and (L25 or L17)	37
<input type="checkbox"/>	L32	correct\$3 with line with address\$3	2055
<input type="checkbox"/>	L31	L29 and (L25 or L17)	3
<input type="checkbox"/>	L30	L25 and (L29 or L17)	53
<input type="checkbox"/>	L29	correct\$3 with bundle with address\$3	20
<input type="checkbox"/>	L28	L25 and L27	3
<input type="checkbox"/>	L27	correct\$3 with L21	463
<input type="checkbox"/>	L26	L21 and L25	61

<input type="checkbox"/>	L25	(712/237 712/238 712/239 712/240).ccls.	799
<input type="checkbox"/>	L24	bundle address\$3 and L17	0
<input type="checkbox"/>	L23	L17 and L21	20
<input type="checkbox"/>	L22	fetch\$3 with correct\$3 with L21	2
<input type="checkbox"/>	L21	block address\$3	9248
<input type="checkbox"/>	L20	L17 and L19	22
<input type="checkbox"/>	L19	correct\$3 with fetch\$3 with (index\$3 or address\$3)	596
<input type="checkbox"/>	L18	L16 and L17	3
<input type="checkbox"/>	L17	(712/234).ccls.	336
<input type="checkbox"/>	L16	(712/206).ccls.	186
<input type="checkbox"/>	L15	(L2 or L3 or L4) same (set of instructions)	0
<input type="checkbox"/>	L14	(L2 or L3 or L4) same thread	8
<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L13	L10 and thread	2
<input type="checkbox"/>	L12	L10 and L11	1
<input type="checkbox"/>	L11	cache adj (block or line)	1748
<input type="checkbox"/>	L10	L7 or L8 or L9	400
<input type="checkbox"/>	L9	plural branches	80
<input type="checkbox"/>	L8	multiple branches	121
<input type="checkbox"/>	L7	plurality adj branches	199
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L6	(L2 or L3 or L4) same L1	3
<input type="checkbox"/>	L5	(L2 or L3 or L4) with L1	1
<input type="checkbox"/>	L4	plurality adj branches	2088
<input type="checkbox"/>	L3	plural branches	111
<input type="checkbox"/>	L2	multiple branches	1108
<input type="checkbox"/>	L1	cache adj (block or line)	7598

END OF SEARCH HISTORY